



World Class SystemVerilog & UVM Training

Sunburst Design - 3-Day UVM Verification Training May 15-17, 2023

Paradigm Works is sponsoring open enrollment UVM Verification training by award-winning UVM trainer, presenter and author, Cliff Cummings. *This course shares UVM Best Known Methods and explains why currently 90%+ of UVM verification engineers are using the wrong database API and why Engineers should use the `uvm_resource_db` API, not the `uvm_config_db` API.* For more information about the training course, pricing, and registration, visit the webpage:

<https://paradigm-works.com/training-courses-menu/>

The training is offered at convenient times for engineers in North America. Lectures typically lasts 5-6 hours plus 1-1/2 hours for Lunch & Lab each day. Cliff will also set aside 1 hour before class and 1+ hours after class to help with labs and answer questions.

LECTURE SCHEDULE

Lecture/Labs scheduled for Eastern U.S. time zones – 10:00 am – 6:00 pm EDT

Lecture/Labs scheduled for Pacific U.S. time zones – 7:00 am – 3:00 pm PDT

Additional Lab Time – Cliff will be on the WebEx call 1 hour before lecture and stay for 1 additional hour after lecture.

COURSE MATERIALS

All materials will be provided as color, searchable, protected PDF files with a 5-year license, and are accessible on laptops running Windows 7 and above.

When enrolling, students need to share their email address and the country where they are located for Paradigm Works to generate the licensed electronic materials.

LABS & TOOLS

Lab files will be provided for download. Students are encouraged to use their own tools to run the labs to gain experience with their preferred tool set.

There are a limited number of training logins that can be provided by Paradigm Works for this training class. Notify Michael Hoyt if you need access to one of the Paradigm Works accounts for training.

PRICING & REGISTRATION

For course Pricing and Registration, visit the web page:

<https://paradigm-works.com/training-courses-menu/>

COURSE CONTENT AND SCHEDULE

For information on the course content and schedule, download in the following PDF document:

UVM_Verification_3day_training.pdf

www.sunburst-design.com/SystemVerilog_Training/UVM_3day_training_202305.pdf