



World Class SystemVerilog & UVM Training

Sunburst Design - 2-Day (4 Half-Days) SystemVerilog Fundamentals Training June 27-30, 2023

Paradigm Works is sponsoring open enrollment SystemVerilog training by award-winning SystemVerilog trainer, presenter and author, Cliff Cummings. For more information about the training course, pricing, and registration, visit the webpage:

<https://paradigm-works.com/training-courses-menu/>

The training is offered as half-day classes to allow engineers from India to North America to attend. Lectures typically lasts 2.5-3 hours each day. Cliff will also set aside 1 hour before class and 1+ hours after class to help with labs and answer questions.

LECTURE SCHEDULE - Time Zones Link:

<https://www.worldtimebuddy.com/?qm=1&lid=8,5780026,5,312,30&h=8&date=2023-6-27&sln=7-10&hf=1>

Lecture scheduled for India – 7:30 pm - 10:30 pm IST

Lecture scheduled for Central Europe – 4:00 pm – 7:00 pm CEST

Lecture scheduled for Eastern U.S. time zones – 10:00 am – 1:00 pm EDT

Lecture scheduled for Pacific U.S. time zones – 7:00 am – 10:00 am PDT

Additional Lab Time – Cliff will be on the WebEx call 1 hour before lecture and stay for 1 additional hour after lecture each day.

COURSE MATERIALS

All materials will be provided as color, searchable, protected PDF files with a 5-year license, and are accessible on laptops running Windows 7 and above.

When enrolling, students need to share their email address and the country where they are located for Paradigm Works to generate the licensed electronic materials.

LABS & TOOLS

Lab files will be provided for download. Students are encouraged to use their own tools to run the labs to gain experience with their preferred tool set.

There are a limited number of training logins that can be provided by Paradigm Works for this training class. Notify Michael Hoyt if you need access to one of the Paradigm Works accounts for training.

PRICING & REGISTRATION

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COURSE CONTENT AND SCHEDULE

For information on the course content and schedule, download in the following PDF document (NOTE: class is taught as 4 half-day sessions):

Systemverilog_Fundamentals_2day_training.pdf

www.sunburst-design.com/SystemVerilog_Training/Systemverilog_Fundamentals_2day_training_rev202303.pdf