



## Clifford E. Cummings

Technical Profile - [www.sunburst-design.com/cliffc](http://www.sunburst-design.com/cliffc)

**Cliff Cummings** is Vice President of Training at Paradigm Works and Founder of Sunburst Design. Cliff has 41 years of ASIC, FPGA and system design experience and 31 years of combined Verilog, SystemVerilog, UVM verification, synthesis, and methodology training experience. Cliff has completed many ASIC designs, FPGA designs and system simulation projects, and can answer the very technical questions asked by experienced design engineers.

Cliff has taught expert Verilog, SystemVerilog, Design, Synthesis, CDC and UVM Verification to thousands of engineers world-wide since 1992 and has presented more than 50 papers on topics related to Verilog, SystemVerilog, synthesis and UVM verification. More than 25 of Cliff's works have been voted "Best Paper" at various conferences.

Cliff has been a presenter, panelist, moderator and featured speaker at conferences, DVClubs and seminars world-wide, including the 2003-2004 world-wide "SystemVerilog NOW!" Seminars, the 2010-2011 world-wide Mentor-sponsored "SystemVerilog Assertions for Verification" Seminars, the featured speaker for the 2018 Accellera Day India on the IEEE 1800.2 UVM Standard, and the 2019 DVCon U.S. Accellera tutorial presenter, also on the IEEE 1800.2 UVM Standard.

Cliff has been a member of the IEEE 1364 Verilog and IEEE 1800 SystemVerilog Standards Groups from 1994 to present-day and was the only Verilog and SystemVerilog trainer to help develop these important IEEE and Accellera Verilog & SystemVerilog Standards.

Prior to founding Sunburst Design, Cliff was employed by Tektronix, Floating Point Systems and IBM where his duties included high-speed ECL and CMOS ASIC design, FPGA design, board design and hardware verification. Cliff received several awards in recognition of his exceptional performance while at Tektronix.

Cliff, who holds a BSEE from Brigham Young University and an MSEE from Oregon State University, is a member of the IEEE, and the Eta Kappa Nu, Tau Beta Pi and Sigma Delta Pi Honor Societies.

Cliff is a recipient of the following professional awards and recognitions:

- IEEE Standards Board Certificate of Appreciation - "In grateful acknowledgement of outstanding contributions to IEEE Std 1800-2005, IEEE Standard for SystemVerilog: Unified Hardware Design, Specification and Verification Language" - November 2005
- [IEEE Computer Society - Golden Core Member - 2003](#)
- IEEE Computer Society Outstanding Contribution Award - "For outstanding technical and leadership contributions to the Working Group developing IEEE Standard 1364.1" - Nov 2003
- IEEE Standards Board Appreciation Award - "for contribution to the development of this standard (IEEE Std 1364-2001), published 28 September 2001"
- Accellera & IEEE Award - for "outstanding efforts in the development of the IEEE 1364-2001 Verilog HDL Language Reference Manual" - June 2001